

LT6911C --- Product Brief

HDMI1.4 to Dual-port MIPI DSI/CSI with Audio

Features

z HDMI1.4 Receiver

- f Compliant with the HDMI 1.4 specification with TMDS data rates up to 3.4Gbps per channel
- f Support HDCP 1.4
- f Adaptive receiver Equalization for PCB, cable and connector losses

z Single/Dual-Port MIPI® DSI/CSI Transmitter

- f Compliant with DCS1.02, D-PHY1.2 & DSI1.02 & CSI-2 1.0
- f 1 Clock Lane, and 1~4 Configurable Data Lanes per port
- f 1/2 configurable port
- f 80Mb/s~1.5Gb/s per data lane
- f Data lane and polarity swapping
- f Maximum 64pixels overlap for each half
- f Both non-burst and burst video mode supported
- f Support RGB666, Loosely RGB666, RGB888, RGB565, 16-bit YCbCr4:2:2, 20-bit YCbCr4:2:2, 24-bit YCbCr 4:2:2, 12-bit YCbCr4:2:0 Video Format
- f Video stream copy mode for each single/dual-port
- f Side-by-side 3D support
- f Port swap

z Miscellaneous

- f 3.3V/1.2V Supply Power
- f Internal CSC support conversions between YCbCr 4:4:4 and RGB, and between YCbCr 4:2:2 and YCbCr 4:4:4
- f Support SPDIF and 2-channel IIS audio output
- f Support 100KHz and 400KHz I2C slave
- f Power from phone or adapter mode selection
- f Integrated Microprocessor
- f Embedded EDID shadow.
- f Temperature Range: -40°C ~ +85°C
- f ESD 4kV HBM

Description

The LT6911C is a high performance HDMI1.4 to MIPI®DSI/CSI chip for VR/Smart phone/Display application.

For MIPI®DSI/CSI output, LT6911C features configurable single-port or dual-port MIPI®DSI/CSI with 1 high-speed clock lane and 1~4 high-speed data lanes operating at maximum 1.5Gb/s/lane, which can support a total bandwidth of up to 12Gbps. LT6911C supports Burst mode DSI video data transferring, also support flexible video data mapping path.

LOW



LT6911C

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